

(1/14)

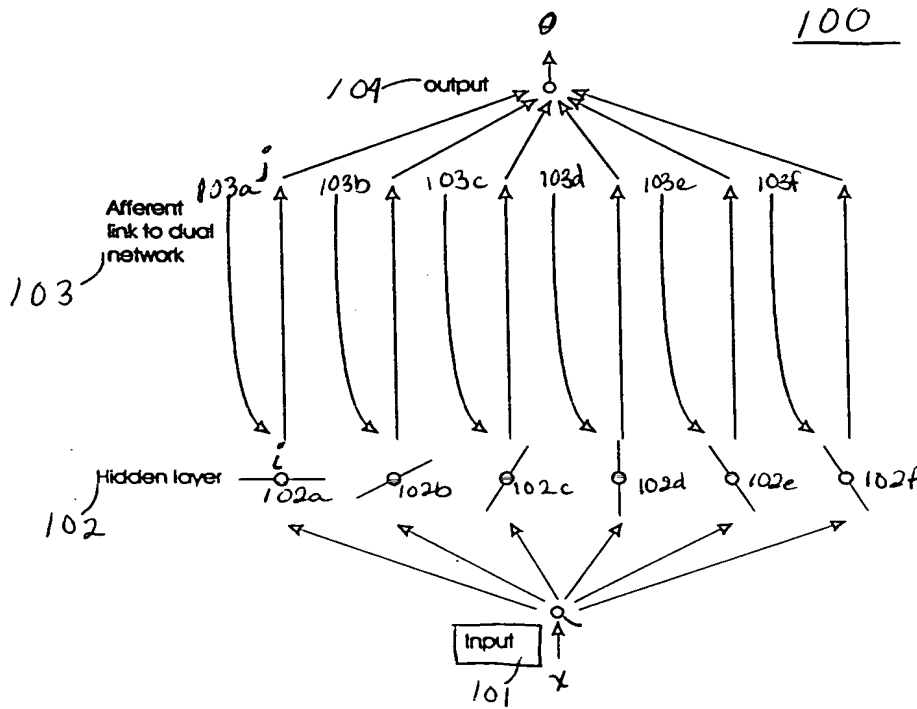


Figure 1

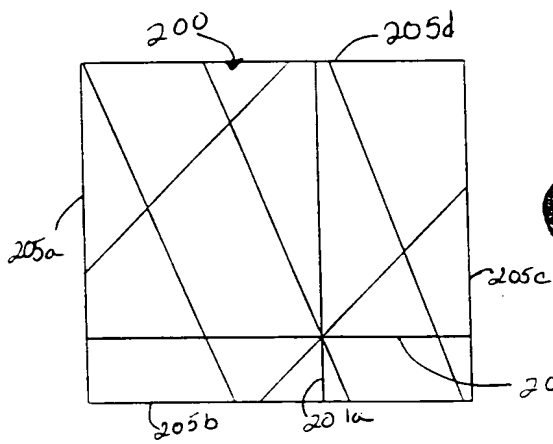


Figure 2a

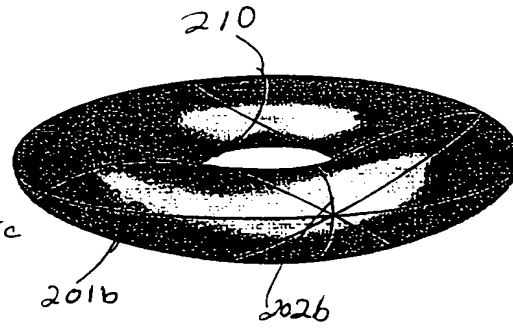


Figure 2b

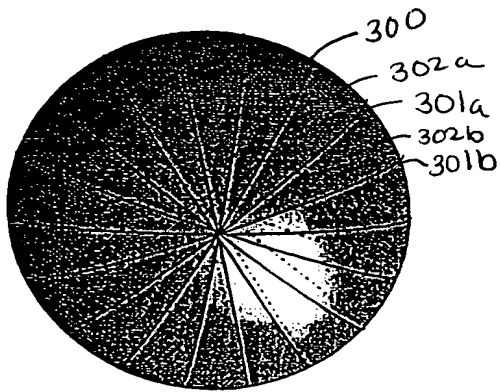


Figure 3a

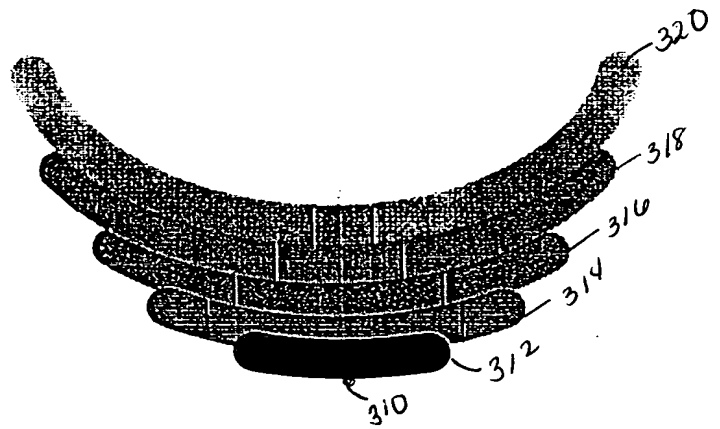


Figure 3b

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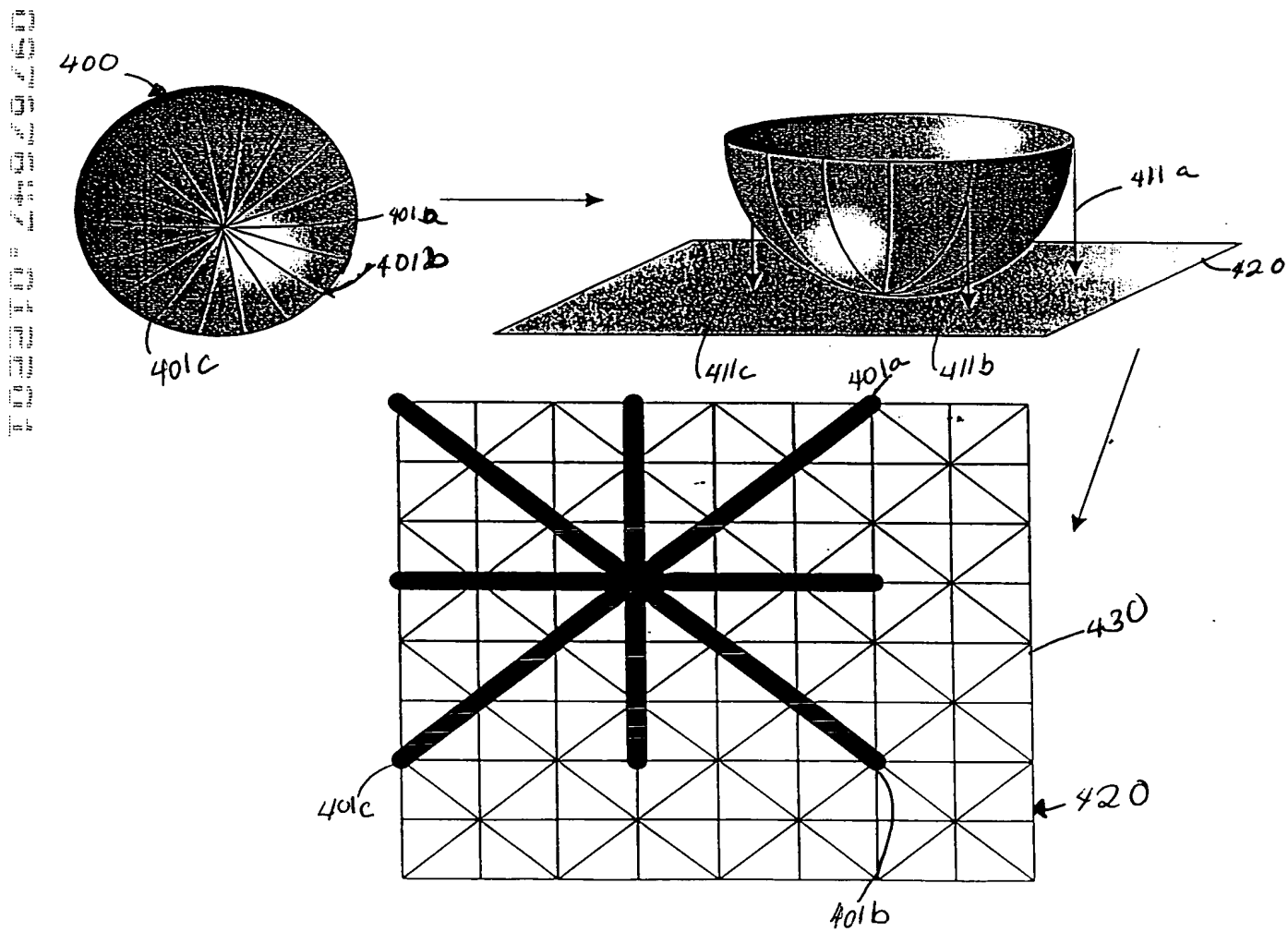


Figure 4

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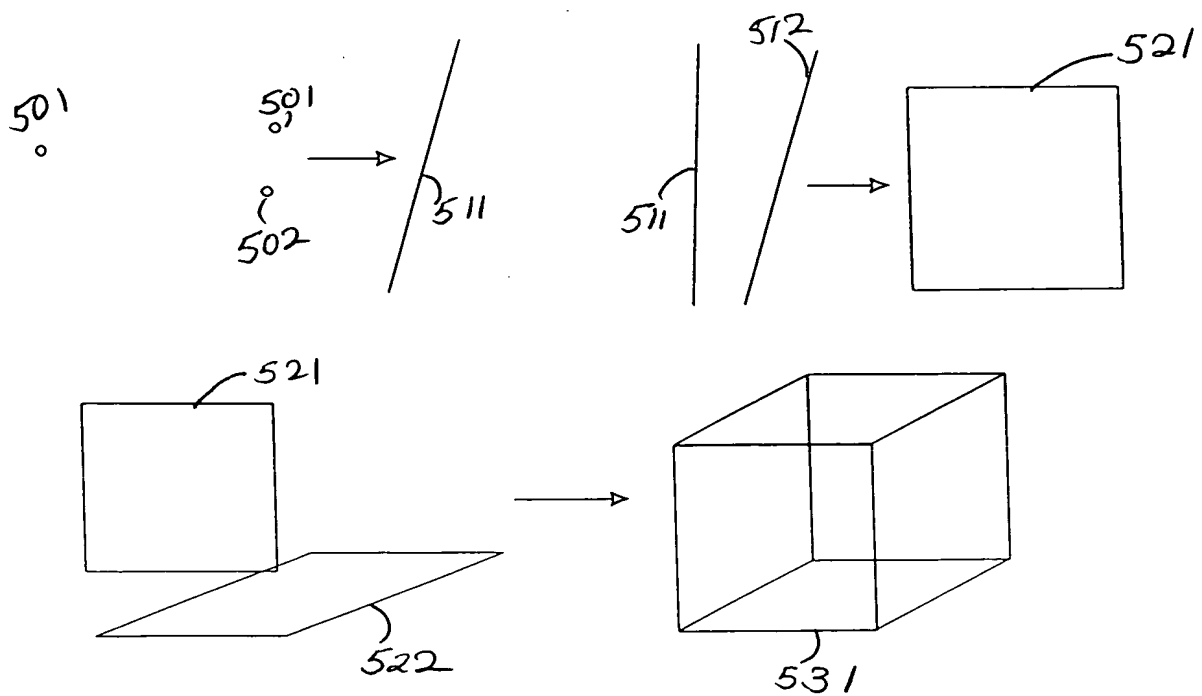


Figure 5

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600

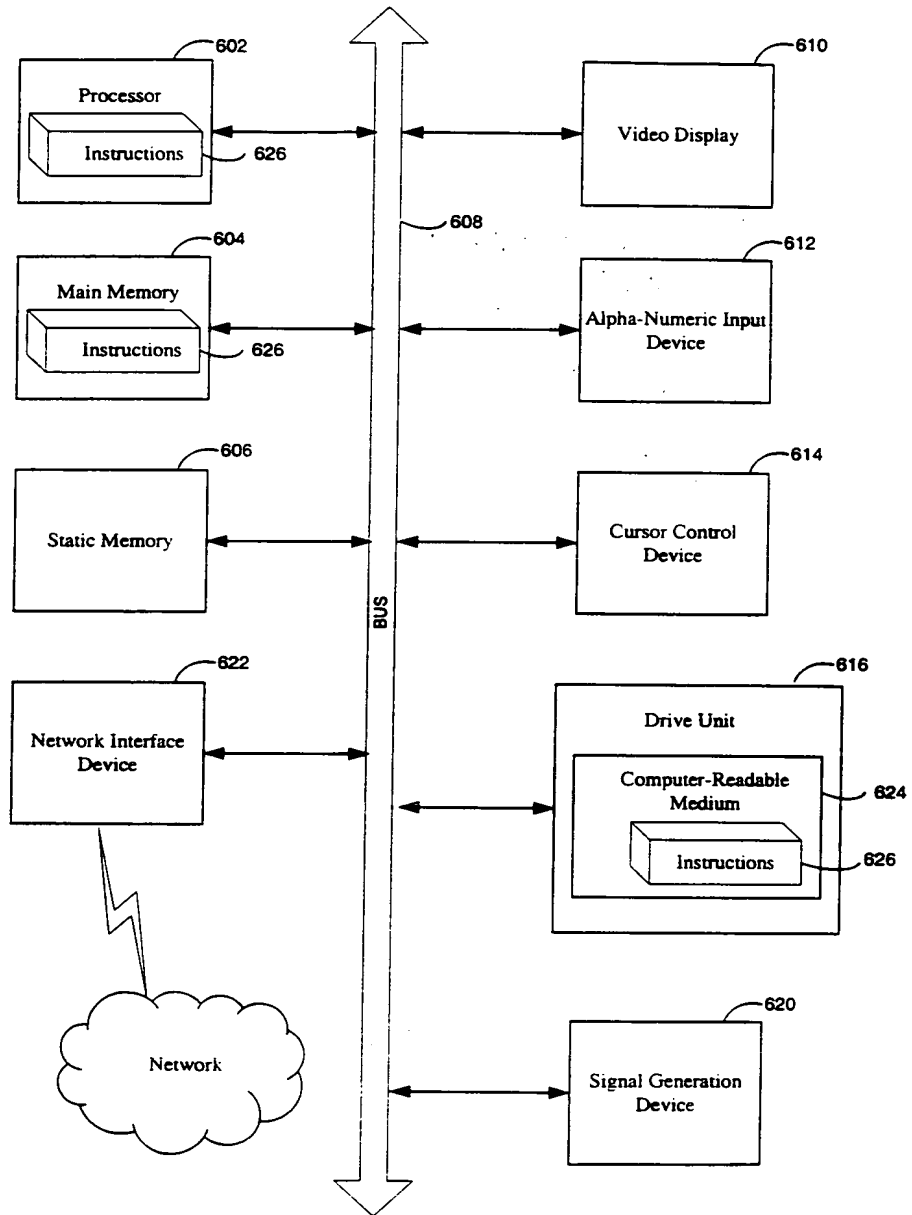


Figure 6

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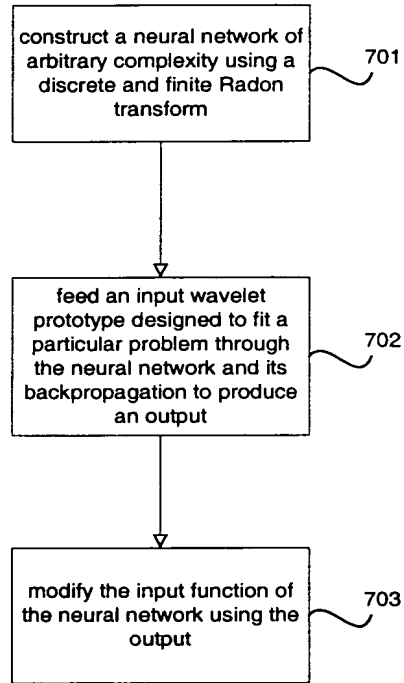


Figure 7

FIG. 8 is a block diagram of a system 800 for generating a neural network. The system 800 includes a Designing Module 810, a Feeder 820, a Neural Network 840, a Neural Network Constructor 830, and a Modifier Module 850. The Designing Module 810 provides an Input Wavelet to the Feeder 820. The Feeder 820 provides an Input Wavelet to the Neural Network 840. The Neural Network Constructor 830 provides an input to the Neural Network 840. The Neural Network 840 provides an output to the Modifier Module 850.

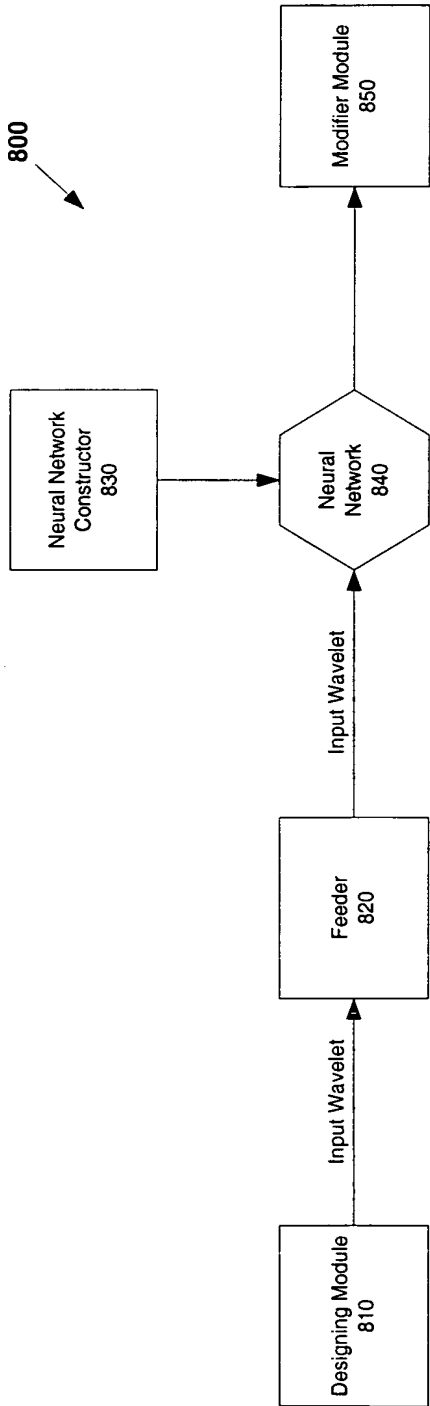


Figure 8



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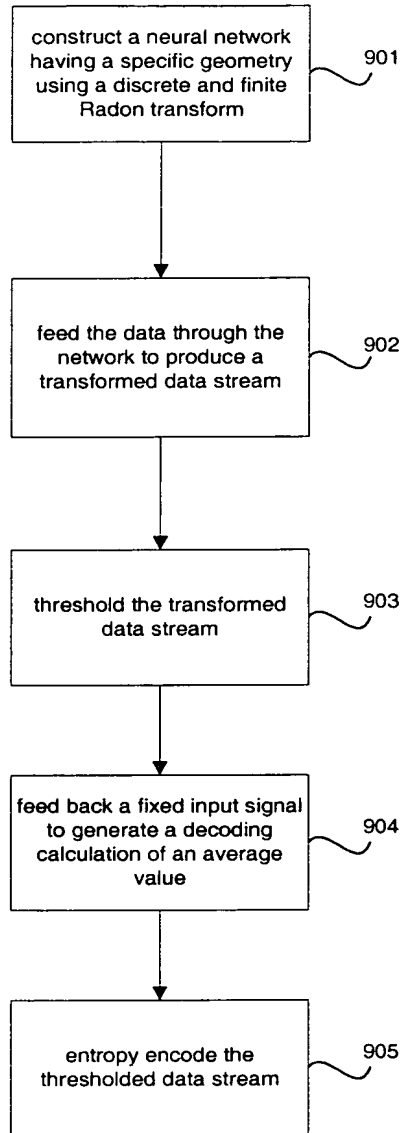


Figure 9

1000

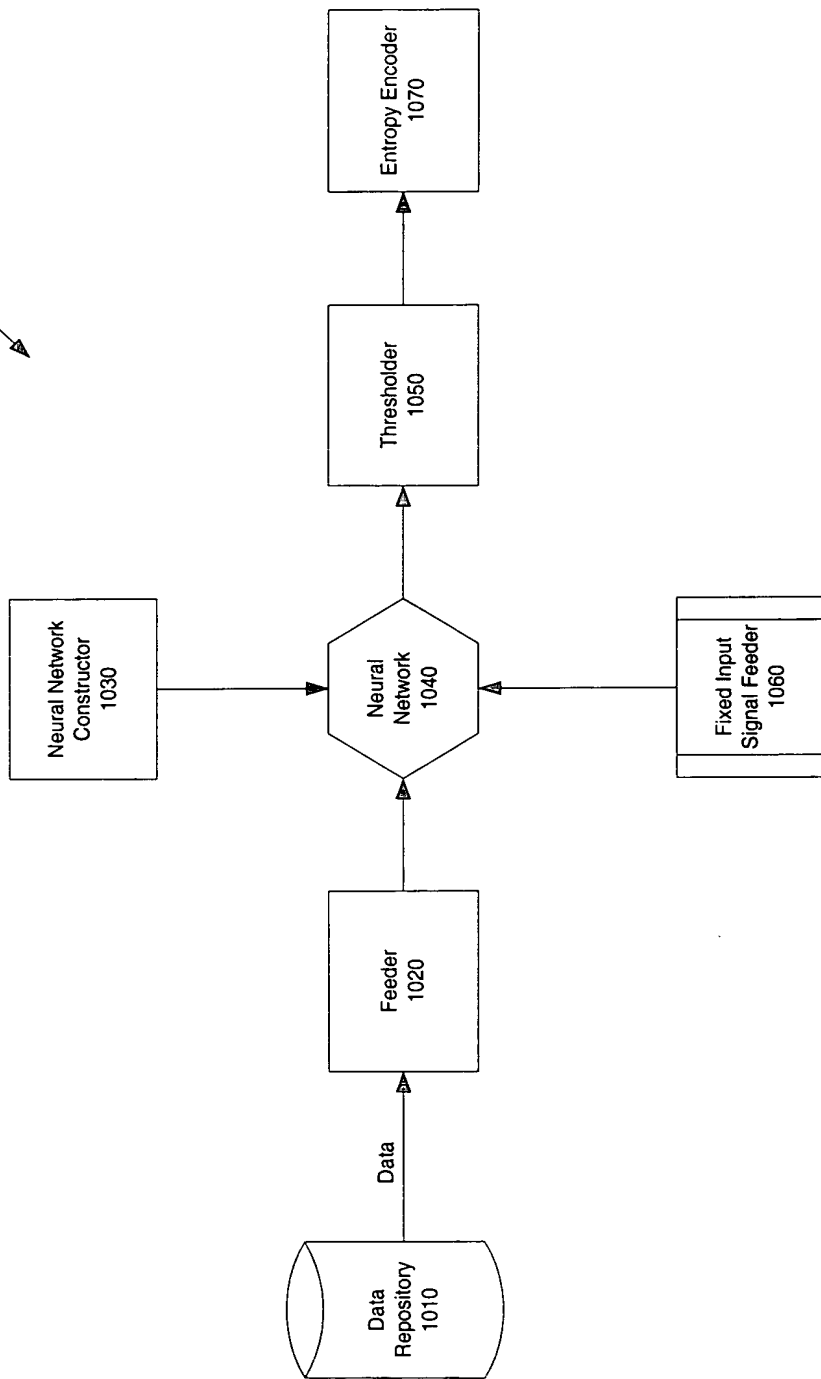


Figure 10

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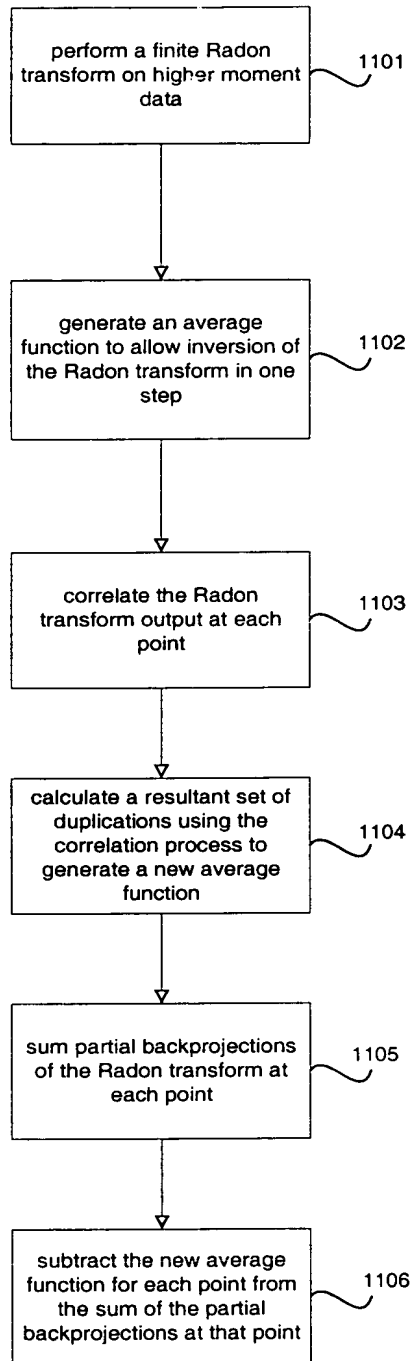


Figure 11

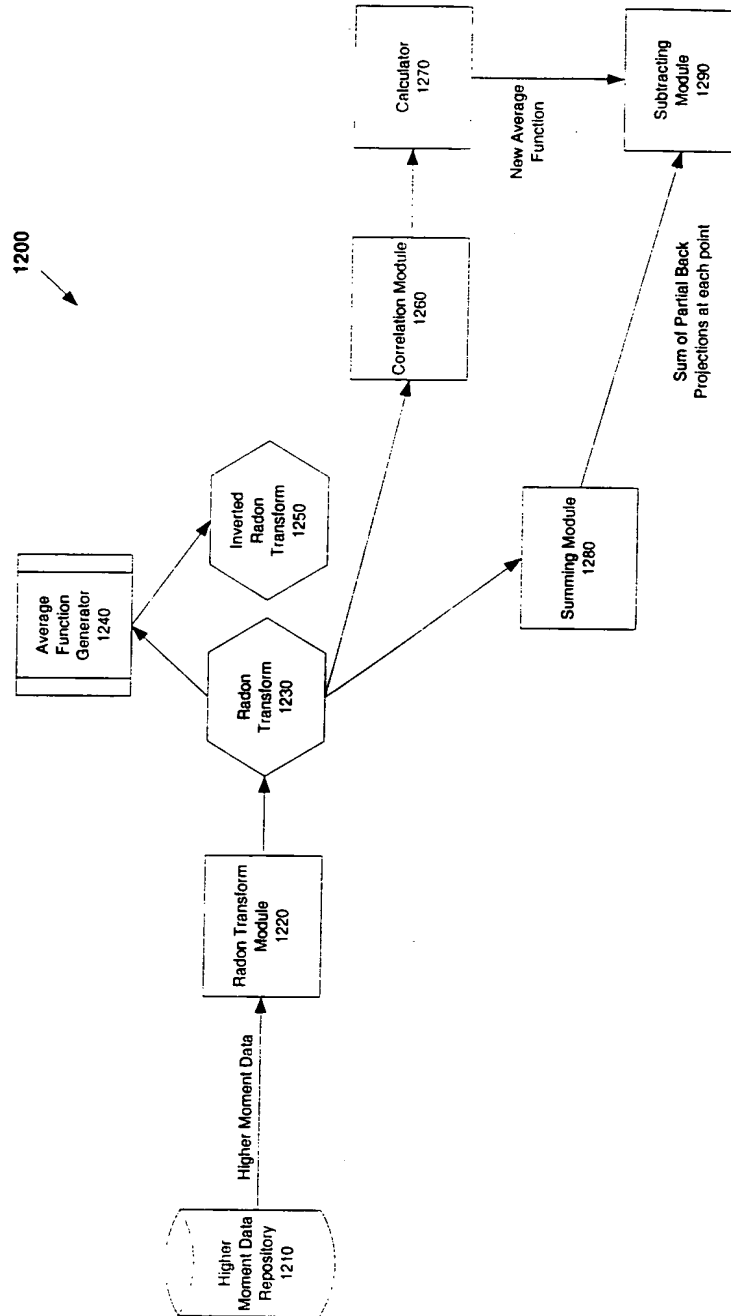


Figure 12

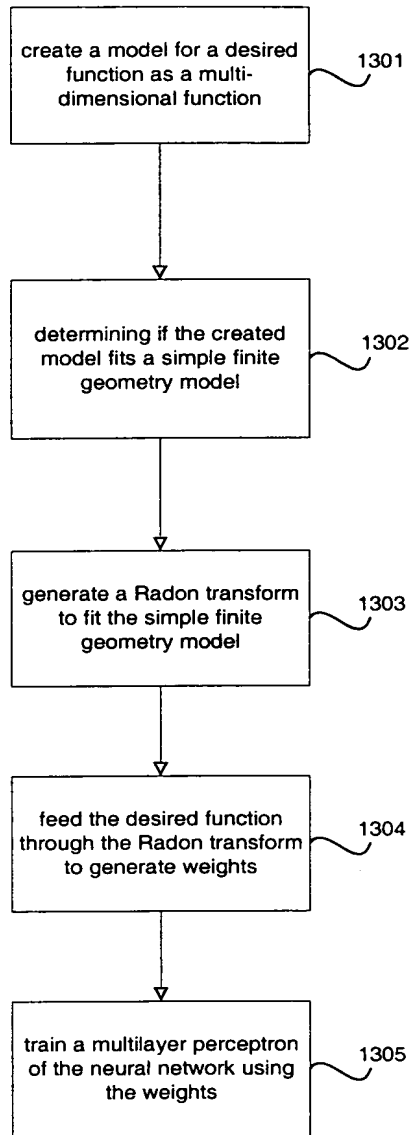


Figure 13

1400

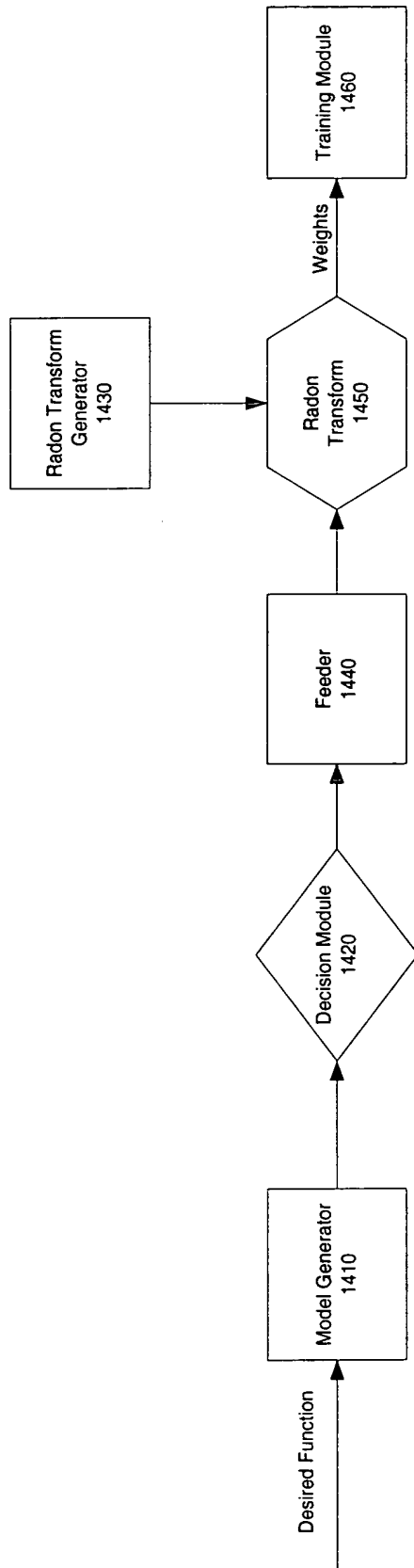


Figure 14